TLE983x BD

Microcontroller with LIN and Power Switches for Automotive Applications

TLE983x

Errata Sheet

Rev. 2.1, 2013-02-26

Automotive Power

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Overview

This document lists the errata of these variants of the TLE983x BD device:

TLE9832QV and TLE9832QX:	TLE9832 Data Sheet Rev. 1.1
TLE9832-2QX:	TLE9832-2 Data Sheet Rev. 1.1
TLE9833QX:	TLE9833QX Data Sheet Rev. 1.1
TLE9834QX:	TLE9834QX Data Sheet Rev. 1.1
TLE9835QX:	TLE9835QX Data Sheet Rev. 1.0

1 Stop Mode Entry and Exit

The Stop Mode needs to be entered and exited with a special configuration of the PLL. Otherwise the system can exit Stop Mode only with Reset.

Workaround:

Before entering Stop Mode the following flag needs to be set just before the execution of the Stop Mode command

```
SFR_PAGE(_su6, noSST);  // switch to page6
OSC_CON |= 0x02;        // set intosc_force to '1'
                        // (switch async to internal PLL clock)

_nop_();                //needed for wakeup
_nop_();
_nop_();
```

Revision History:	2.1, 2013-02-26
Previous Version:	2.0, 2012-07-27
Major Changes:	Chapter 3 and 4.4 added

Directly after exiting Stop Mode the setting has to be cleared by using the following commands:

```
SFR_PAGE(_su6, noSST);  // switch to page6
OSC_CON &= ~0x02;       // set intosc_force to '0'
                        // (PLL internal oscillator OSC_PLL is
                        // selected synchronously)
```

Please note: Register OSC_CON is password protected. Before it can be modified it needs to be unlocked.

Planned fix:
description will be updated in the User Manual. No fix in silicon planned.

2 Temperature coefficients

The temperature coefficients in the corresponding data sheets are not valid.
The valid coefficients are:

Output voltage V_{Temp} at T_0=273 K (0°C)	0.4955 V (was 0.5365 V before)	P_5.8.23
Temperature sensitivity b	1.807 mV/K (was 1.834 mV/K before)	P_5.8.25

Workaround:
use the valid coefficients in software to calculate the junction temperature.

3 Edge Detection on GPIOs requires rise time < 15µs

The edge detection on GPIO (P0.x, P1.x, P2.x) input signals requires a rise time of < 15µs (20%-80% of 5V).

Errata description (functional limitation) for rise times > 15µs:
A rising edge is always signalled correctly as rising edge, but occasionally signalled as falling edge as well.
The opposite case works as expected: A falling edge is always signalled correctly as falling, and never signalled as rising edge.
The wrongly detected falling edges have influence on following functionality:

- Interrupt generation
- Edge counter
- Timer start based on detected edge

All functional blocks that evaluate falling edges on GPIO input signals are affected, e.g.

- CCU6 in capture mode
- Timer
- external interrupts (EXTINT)
- UART

Workaround:

First recommendation: ensure an input signal rise time of < 15µs.

If this is not possible, 3 cases need to be distinguished:

In case only rising edges are evaluated, no workaround is required.

In case only falling edges are evaluated, the errata described above occurs. No workaround is available for this case.

In case both edges are used for interrupt generation, but depending on the edge different actions should be taken (e.g. in CCU6), following workaround can be used:

Table 1

rising edge signalled	falling edge signalled	action
no	no	no action
no	yes	handle as falling
yes	no	handle as rising
yes	yes	handle as rising

4 Application hints

4.1 LIN overcurrent shutdown

In the design step BD, the automatic LIN overcurrent shutdown has been removed. The LIN transceiver is not disabled automatically any more, the protection is ensured by the overcurrent limitation feature.

The overcurrent events is signalled in LIN_OC_STS, and needs to be handled by software (interrupt or polling), in case it appears in the specified LIN operational range.

4.2 LIN Transceiver enabling with slow ramping

With the default setting of register PCU_CTRL_STS_2, the LIN Transceiver is disabled whenever VS under voltage condition is met (VS <= 5.5V). It has to be ensured by software sequence, that only after leaving the VS under voltage condition (VS > 5.5V), which can be checked by software, the LIN module will be properly enabled.

Implementation proposal:

1) check bit SYS_SUPPLY_STS. VS_UV_STS to be 0

2) then write 0x00 to register LIN_CTRL_STS_1

3) then write 0x06 to register LIN_CTRL_STS_1 (LIN normal mode)

4.3 Temperature calculation

The formula to be used to calculate the junction temperature from the ADC output value is:

Tj [°C] = ((ADC_out/256*1.23/1.24)-0.4955)*1000/1.807

with

1.23 Ref-Voltage ADC2 [V]

1.24 gain at differential input of ADC

0.4955 and 1.807 Temperature coefficients, see chapter “Temperature coefficients”

4.4 VDDEXT does not automatically switch off in case of short circuit

In case of short circuit, the short circuit condition is signalled in VDDEXT_CTRL.VDDEXT_SHORT, but the VDDEXT is not switched off automatically.

Switching off needs to be done explicitly in customer software (VDDEXT_CTRL.VDDEXT_ ENABLE = 0).

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